

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Verdun, Gary J.		994	Examiner: Butler, Dennis
Serial No.	09/707,585	8	Group Art Unit: 2115
Filed:	November 6, 2000	79	Notice of Allowance Date: January 26, 2005
For: PROCESSOR POWER STATE TRANSITIONS USING SEPARATE LOGIC CONTROL		999	Confirmation No.: 9642

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312

MAIL STOP ISSUE FEE Commissioner For Patents P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Enter DMB

11-17-05

Please amend the above-identified application as follows: